IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Toru ISHIKAWA

Serial No.:

Not Yet Assigned

Group Art Unit:

Not Yet Assigned

Filing Date:

Concurrently Herewith

Examiner:

Unknown

For:

DLL CIRCUIT

Honorable Commissioner of Patents Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following document listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. A copy of the listed document is provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the reference is relevant or material to the claims. It is only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed reference be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

Sean M. McGinn, Esq. Registration No. 34,386

Date:

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INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)					Docket Number (Optional) NEG-332US		Application Number Not Yet Assigned			
					Applicant(s) Toru Ishikawa	Applicant(s)				
					Filing Date Group Art Unit		Group Art Unit Not Ye	Yet Assigned		
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		2002 VLS1 Symposium:	Thesis number s	9-1.						
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